



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Leonard Forbes

**Serial No.:** 09/943,968

**Filed:** August 30, 2001

**For:** TECHNIQUE TO SIMULTANEOUSLY  
DISTRIBUTE CLOCK SIGNALS AND DATA  
ON INTEGRATED CIRCUITS,  
INTERPOSERS, AND CIRCUIT BOARDS

**Examiner:** Unknown

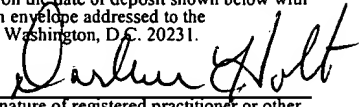
**Group Art Unit:** 2631

**Attorney Docket No.:** 4720US (00-1120)

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Darlene Holt  
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**Technology Center 2600**

**TRANSMITTAL OF FORMAL DRAWINGS**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Attached please find the formal drawings for this application.

Respectfully submitted,



Devin R. Jensen  
Registration No. 44,805  
Attorney for Applicant  
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Telephone: (801) 532-1922

Date: January 10, 2002

DRJ/hlg:dh

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**LETTER TO THE CHIEF DRAFTSMAN**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicant submits herewith revised FIG. 9 which corrects an error in the drawing. Specifically, FIG. 9 has been revised to add the reference numeral "R5." Attached is a copy of the drawing with the proposed change marked in red.

Serial No.: 09/943,968

No new matter has been added. Approval of the proposed revision is respectfully requested.

Respectfully submitted,



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P. O. Box 2550  
Salt Lake City, Utah 84110-2550  
Telephone: (801) 532-1922

Date: January 10, 2002

DRJ/hlg:dh

Enclosures: Drawing with change marked in red

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